ReCoSoC

14th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC 2019)

July 1-3 2019, York - United Kingdom

Call for Papers

http://www.recosoc.org/

Over the past decade ReCoSoC has established itself as a reference international event for research in the areas of reconfigurable and communication-centric systems-on-chip. Its informal and dynamic philosophy encourages the technical and scientific interactions between senior academics, young researchers and industry practitioners. In 2019, ReCoSoC will be held at the historic city of York, United Kingdom.

Authors are invited to submit contributions as maximum 8 page papers in IEEE conference format. Contributions have to be submitted electronically through the conference website. As in previous editions, all accepted papers will be submitted for inclusion in IEEE Xplore.

Areas of interest

- New paradigms for communication-centric, adaptive and reconfigurable computing
- Reconfigurable and adaptive embedded SoCs
- FPGAs and reconfigurable hardware accelerators for HPC, cloud and machine learning
- On-chip communication architectures (buses and networks-on-chip - NoCs)
- Communication-centric design techniques at different abstraction levels
- Communication-aware multiprocessor embedded systems
- Low power design of reconfigurable and multiprocessor SoCs
- Run-time thermal and power management
- OS and middleware for reconfigurable and multicore SoCs
- FPGAs and reconfigurable hardware for internet-of-things (IoT)
- Approximate computing in FPGAs
- FPGAs and reconfigurable computing for software programmers
- Specification languages and design methodologies supporting the topics above
- Verification and evaluation techniques supporting the topics above
- Industrial case studies applying the topics above to domains such as HPC, networking, telecom, cloud computing and transportation systems

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Abstract submission deadline	April 28, 2019
Full paper submission deadline	May 5, 2019
Notification of acceptance	June 4, 2019